



EXAMINATION SECTION

ACADEMIC YEAR: 2025-26

Revised Time Table - M.Tech. I Year II Semester (R25) Regular End Semester Examinations June 2026

TIMINGS: FN: 10:00 A.M to 01:00 PM

Date/Day	Session	Computer Science and Engineering (CSE)	VLSI Design and Embedded Systems (VES)
22.06.2026 (Monday)	FN	Advanced Machine Learning-25MBCSETC03	CMOS Analog IC Design-25MBVESTC03
24.06.2026 (Wednesday)	FN	Advanced Data Visualization Techniques- 25MBCSETC04	Embedded System Design-25MBVESTC04
29.06.2026 (Monday)	FN	Responsible AI-25MBCSEDC10	High Speed VLSI Design-25MBVESDC12
01.07.2026 (Wednesday)	FN	Cyber Security-25MBCSEDC09	Pattern Recognition and Machine Learning- 25MBVESDC07

Dated: 16.06.2026

Controller of Examinations

To be read in all the M. Tech-(CSE, VES) I Year II Semester Classrooms

Copy to The Website The File Notice Board HoD-CSE HoD-ECE

Note: HoD is requested to circulate among the faculty members concerned

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE - 517 325, A. P.
Transport Incharge